



The HTU2X provides a device specific serial number which can be read-out via the serial interface (I2C) and allows for an unambiguous identification of each individual device. This application note describes the procedure for read-out of the serial number.

COMMUNICATION SEQUENCE

The communication sequences for reading out the serial number comply with the general I2C serial interface protocol. For detailed information on I2C, please refer to the Datasheet HTU2XD (HPC199 document).

The serial number of the HTU2X is partitioned to two different locations on the on-chip memory. Therefore, two memory access sequences are required to retrieve the complete serial number information. Figure below shows the corresponding communication sequence.

First memory access:

ACK ACK SDA S 0 I2C address 0 0 0 0 I2C address + write ACK S I2C address I2C address + read ACK ACK ACK **CRC** SNB_3 **CRC** SNB_2 ACK ACK ACK **CRC CRC** SNB_1 SNB_0



Second memory access:

ACK **SDA** S 0 0 I²C address W 1 1 1 1 1 0 1 1 0 0 I2C address + write S I2C address I2C address + read ACK ACK SNC_1 SNC_0 **CRC** NACK ACK ACK CRC SNA_1 SNA_0

S is for I2C start condition
P is for I2C stop condition
Data in bold are data from HTU2X sensor to master

COMPOSITION OF SERIAL NUMBER

After reading out the data as described above, the serial number is obtained by arranging the data bytes according to figure below.

64 bits								
16 bits		32 bits				16 bits		
SNA_1	SNA_0	SNB_3	SNB_2	SNB_1	SNB_0	SNC_1	SNC_0	
0x48*	0x54*	0x00*	0xXX	0xXX	0xXX	0x32*	0xXX	

^{*} are fixed values



EXAMPLE

For illustration of the read-out procedure, a numeric example is presented below. The I2C address of the HTU2X is assumed as "0100'0000" (hexadecimal: 0x40).

The serial number of the numeric example reads (hexadecimal) is 0x4854'0054'3210'3212.

First memory access:

ACK SDA S 0 0 0 0 0 0 0 0 0 0 0 0 P S O ACK o \X **CRC** 0 0 0 0 0 0 **CRC** 0 0 0 0 1 0 SNB_3 SNB_2 NACK ACK ACK ACK 0 0 1 1 0 0 0 **CRC** 0 0 0 1 0 0 0 0 **CRC** SNB_1 SNB_0



Second memory access:

ACK ACK **SDA** S ACK S ACK ACK **CRC** SNC₁ SNC 0 ACK ACK CRC SNA 1 SNA 0

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